

LIST OF PUBLICATIONS

1. Khalil Ibrahim Mahmoud, **Dhurga Devi, J** & Ramakrishna, PV 2011, 'Design and Analysis of a Dual Loop CDR using Maneatis Delay Cell VCO', The Fourth International Conference on Advances in Circuits, Electronics and Micro-electronics (CENICS 2011), French Riviera, France.
2. **Dhurga Devi, J** & Ramakrishna, PV 2011, 'Performance improvement for Maneatis PLL for Microprocessor Clock', published in the proceedings of 7th International conference on Ph.D Research In Microelectronics and Electronics, Trento, Italy.
3. **Dhurga Devi, J** 2013, 'Design of Delay Interpolator based Differential Self biased PLL', Journal of theoretical and Applied Information Technology, vol.58. no.3., pp 582-588.