<table>
<thead>
<tr>
<th>Sr. No.</th>
<th>Abbreviations</th>
<th>Meanings</th>
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</thead>
<tbody>
<tr>
<td>1</td>
<td>ALU</td>
<td>Arithmetic and Logic Unit</td>
</tr>
<tr>
<td>2</td>
<td>AIX</td>
<td>Advanced Interactive Executive</td>
</tr>
<tr>
<td>3</td>
<td>COMA</td>
<td>Cache Only Memory Architecture</td>
</tr>
<tr>
<td>4</td>
<td>CPU</td>
<td>Central Processing Unit</td>
</tr>
<tr>
<td>5</td>
<td>CRL</td>
<td>C Region Library</td>
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<tr>
<td>6</td>
<td>DDM</td>
<td>Data Diffusion Machine</td>
</tr>
<tr>
<td>7</td>
<td>DSM</td>
<td>Distributed Shared Memory</td>
</tr>
<tr>
<td>8</td>
<td>DSMC</td>
<td>Distributed Shared Memory Controller</td>
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<tr>
<td>9</td>
<td>EC</td>
<td>Entry Consistency</td>
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<tr>
<td>10</td>
<td>GDAS</td>
<td>Global Distributed Address Space</td>
</tr>
<tr>
<td>11</td>
<td>GUI</td>
<td>Graphical User Interface</td>
</tr>
<tr>
<td>12</td>
<td>H/W</td>
<td>Hardware</td>
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<td>13</td>
<td>IPC</td>
<td>Interprocess Communication</td>
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<td>14</td>
<td>IVY</td>
<td>Integrated Shared Virtual Memory at Yale</td>
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<tr>
<td>15</td>
<td>KSR</td>
<td>Kendall Square Research Corporation</td>
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<tr>
<td>16</td>
<td>LAN</td>
<td>Local Area Network</td>
</tr>
<tr>
<td>17</td>
<td>MemNet</td>
<td>Memory as Network Abstraction</td>
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<tr>
<td>18</td>
<td>MMU</td>
<td>Memory Management Unit</td>
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<td>19</td>
<td>MP</td>
<td>Multi Processors</td>
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<tr>
<td>20</td>
<td>MRMW</td>
<td>Multiple Read Multiple Write</td>
</tr>
<tr>
<td>21</td>
<td>MRSW</td>
<td>Multiple Read Single Write</td>
</tr>
<tr>
<td>22</td>
<td>NUMA</td>
<td>Non Uniform Multiple Access</td>
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<td>23</td>
<td>OS</td>
<td>Operating System</td>
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<td>PVM</td>
<td>Parallel Virtual Machine</td>
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<td>25</td>
<td>RMS</td>
<td>Reflective Memory System</td>
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<td>27</td>
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<td>28</td>
<td>SMP</td>
<td>Symmetric Multiprocessors</td>
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<tr>
<td>29</td>
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<td>30</td>
<td>S/W</td>
<td>Software</td>
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<tr>
<td>31</td>
<td>UMA</td>
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<td>32</td>
<td>VM</td>
<td>Virtual Machine</td>
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<td>33</td>
<td>WAN</td>
<td>Wide Area Network</td>
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