APPENDIX A

PSPICE simulation diagrams

PSPICE simulation circuit of low pass filter using simulated inductor shown in Figure 2.5
PSPICE simulation circuit of high pass filter using simulated inductor shown in Figure 2.8
PSPICE simulation circuit of band pass filter using simulated inductor shown in Figure 2.11
PSPICE simulation circuit of notch filter using simulated inductor shown in Figure 2.14
PSPICE simulation circuit of Bessel low pass filter using simulated inductor shown in Figure 3.2
PSPICE simulation circuit of Bessel high pass filter using simulated inductor shown in Figure 3.5
PSPICE simulation circuit of Bessel all pass filter using simulated inductor shown in Figure 3.8
PSPICE simulation circuit of low pass filter using FDNR – Method I shown in Figure 4.3
PSPICE simulation circuit of low pass filter using FDNR – Method II shown in Figure 4.4
PSPICE simulation circuit of Notch filter using FDNR shown in Figure 4.8
PSPICE simulation circuit of single tuned amplifier using simulated inductor shown in Figure 5.1
PSPICE simulation circuit of Hartley oscillator using simulated inductor shown in Figure 5.3
PSPICE simulation circuit of Colpitts oscillator using simulated inductor shown in Figure 5.4
PSPICE simulation circuit of active tuned oscillator using simulated inductor shown in Figure 5.10
PSPICE simulation circuit of Class D audio power amplifier using simulated inductor shown in Figure 6.2
PSPICE simulation circuit of high pass filter using the newly proposed SUJA simulated inductor shown in Figure 7.4
PSPICE simulation circuit of band pass filter using the newly proposed SUJA simulated inductor shown in Figure 7.6
PSPICE simulation circuit of notch filter using the newly proposed SUJA simulated inductor shown in Figure 7.8
PSPICE simulation circuit of oscillator using the newly proposed SUJA simulated inductor shown in Figure 7.10
PSPICE simulation circuit of tuned amplifier using the newly proposed SUJA simulated inductor shown in Figure 7.12