ACKNOWLEDGEMENT

I record my deep sense of gratitude with extreme honor to Dr. Suresh Chandra Mohan, Professor and Former Principal, University B D T College of Engineering, Davangere, for his guidance, keen interest, constructive criticisms, critical appraisal of the manuscript and advice during the course of this research work.

I wish to extend my deep sense of gratitude to the Director and the Principal, Bapuji Institute of Engineering and Technology, Davangere for their support during this work.

I am thankful to the HOD of Electronics and Communication Engineering, Bapuji Institute of Engineering and Technology, Davangere, for the encouragement. I thank all the teaching faculty and non-teaching staff of the Department, for their co-operation during the work. In particular, I thank Smt. C. R Nirmala, Assistant Professor, Department of Computer Science and Engineering, for her assistance during the course of this work. I thank the librarian and his staff for extending digital library facilities.

I specially thank OPNET Technologies, Inc. for having provided the simulation tool to carry out my work from 2006-2010.

My special thanks for the loving support and sacrifices of my beloved father Late Sri G. H Sadashivappa, my mother Smt. Kusuma, father in-law Late Sri. Somanagouda. S. Chikkanagoudar, mother in-law Smt. Parvathadevi and all my elders and relatives. I would like to acknowledge the moral support of my brothers and sisters. It would be incomplete without acknowledging my brother Mr. Arun B. C, Computer Programmer, J. J. M. M. C, Davangere for his valuable help during work.

I specially thank my beloved husband Sri Ravindranath, my daughter Sahana and my son Basavaraj for their cherished, unconditional support and, patience, without which I could not have undertaken this work.

Finally I would like to thank all those who have helped me directly or indirectly during the course of this research work.

Smt. G. S Sunitha