The DST bit consists of a series of First Level Triggers (FLTs) and Second Level Triggers (SLTs) in OR configuration. The DST14 filter requires an $E - p_z + 2E_y$ cut of 35 GeV, an electron with energy greater than 5 GeV and a box cut with SRTD position of 13 cm. DST14 is an OR of TLTNC and TLTLQ filters where

1. TLTNC has TLT Bits 3, 4, 6, 7, 8, 9, 10, or Exotic TLT bits 4, 5 or 7
2. TLTLQ has TLT Bits 1 or 2

The TLT and Exotic TLT bits mentioned above require following FLT and SLT slots in OR conditions.

1. SLT slots : SLT 1, 2, 3, 4, 5, 6, 7 OR 8
2. FLT slots : FLT 28, 30, 40, 41, 43, 44, 46 or 47 and there are other FLT bits from the above SLT slots like FLT 31, 39, 42, 50 or 60.

All the above mentioned FLT's have Track Veto conditions except FLT30 slot. The Track Vetoes are classified as Loose (LTV), Semi-Loose (SLTV) and Tight (TTV) depending on the relative number of vertex track and total track multiplicities. A brief description of the Track vetoes is as given below.

1. Loose Track Veto : Track Class not equal to 2.
2. Semi-Loose Track Veto: Track Class not equal to 2 OR track class not equal to 8 with track multiplicity greater than 26.

3. Tight Track Veto: Track Class not equal to 2 OR 8

FLT 31 and FLT 44 are the slots that require TTV condition but there is no event in the final selection that gets exclusively triggered by these FLTs. All other FLTs require either of SLTV and LTV for firing except FLT30. The efficiency of SLTV and LTV is studied with respect to FLT30 slot as it has no track veto requirement. Only 2.8% of the total events are fired by FLT30 as one of the OR conditions in this slot is an isolated electron in RCAL.