CONTENTS

ACKNOWLEDGEMENTS

CHAPTER - 1 TRAFFIC STUDIES
1.1 Importance of Traffic Management under Indian Conditions 01
1.2 Traffic Surveys 02
1.3 Traffic Controls 07
1.4 Traffic Signals 09
1.5 Theory of Traffic flow 13
1.6 The Queueing Theory 15
1.7. Analysis of the Traffic flow 18

CHAPTER - 2 THE MICRO COMPUTER SYSTEM
2.1 General Aspects 20
2.2 Arithmetic Logic Unit 22
2.3 Control Unit 23
2.4 Memory 24
2.5 Input / Output Devices 26
2.6 Organization of Microcomputers 27

CHAPTER - 3 8085 MICROPROCESSOR AND INTERFACING WITH THE PERIPHERALS
3.1 Microprocessor Architecture 31
3.2 Various Signals of 8085 33
3.3 INTEL 8085 Instructions 37
3.4 Timing and Machine Cycles of 8085 38
3.5 Input / Output Ports 41
3.6 Programmable Peripheral Interface 42
3.7 Programmable Interrupt Controller 46
3.8 Programmable DMA Controller 49
3.9 Programmable Communication Interface 53
3.10 Programmable Internal Timer 55
3.11 Analog Subsystems 59

CHAPTER - 4 MICROPROCESSOR BASED POPULATION DENSITY DEPENDENT TRAFFIC CONTROLLER
4.1 Introduction 62
4.2 Hardware 63
4.3 Software 65
4.4 Software Listing 73

Conclusions 84

References