## CONTENTS

<table>
<thead>
<tr>
<th>Page No.</th>
</tr>
</thead>
<tbody>
<tr>
<td>Preface</td>
</tr>
</tbody>
</table>

### CHAPTER-1: Introduction

1.1 Introduction to embedded system  
1.2 Programmable Logic Devices (PLDs)  
   1.2.1 History of PLDs  
   1.2.2 Programming PLDs  
1.3 Field Programmable Gate Arrays (FPGAs)  
   1.3.1 Introduction  
   1.3.2 What is FPGA?  
   1.3.3 Architecture of FPGA  
   1.3.4 How FPGA Works?  
   1.3.5 FPGA Design Flow overview  
   1.3.6 Advantages of FPGAs  
1.4 Hardware Description Language  
   1.4.1 What is HDL?  
   1.4.2 Introduction to VHDL  
   1.4.3 Types of Representation  
   1.4.4 VHDL Characteristics  
   1.4.5 The Scope of VHDL  

### REFERENCES  

18

### CHAPTER-2: Literature review

2.1 Introduction  
2.2 Literature review  
2.3 Introduction to Heart Beat measurement  
2.4 Overview of Heart rate and Measurement methodology  
   2.4.1 What is heart rate?  
   2.4.2 Measurement of heart rate system  
2.5 Block diagram of Heart rate measurement system
CHAPTER-3: Hardware and Software Descriptions

3.1 Introduction
3.2 Hardware Details
   3.2.1 Schematic diagram
   3.2.2 Heart Beat Sensor
   3.2.3 Working
3.3 Spartan-3 FPGA Descriptions
3.4 FPGA Base Board
   3.4.1 Daughter Board
   3.4.2 XCF02 (Platform Flash PROM)
   3.4.3 Downloading Tool
   3.4.4 Download Cables
   3.4.5 Joint Test Action Group (JTAG)
3.5 Software Features
3.6 Xilinx ISE9.1i (EDA Tool)
   3.6.1 Design Entry
   3.6.2 Assigning Package Pins
   3.6.3 Synthesis
   3.6.4 Implementation
   3.6.5 Verification
   3.6.6 Device Configuration
   3.6.7 Configuration Overview

REFERENCES