



CENTRE FOR RESEARCH
ANNA UNIVERSITY :: CHENNAI 600 025



CERTIFICATE

This is to certify that all corrections and suggestions pointed out by the Indian /Foreign Examiner(s) are incorporated in the Thesis titled **“CERTAIN INVESTIGATIONS ON PERFORMANCE ANALYSIS OF VLSI IMPLEMENTATION OF AREA EFFICIENT MULTIPLIER ACCUMULATOR UNIT FOR FIR FILTER”** submitted by **Mr. M. JAYAPRAKASH (Reg. No. 4081052109)**.

Signature of the Joint Supervisor

Signature of the Supervisor

Place: Coimbatore

Date: 13. 10. 2014



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PROCEEDINGS OF THE Ph.D VIVA-VOCE EXAMINATION OF Mr.M.JAYAPRAKASH HELD AT 3.00 PM ON 13-10-2014 IN SNS COLLEGE OF TECHNOLOGY, COIMBATORE - 641 035.

The Ph.D. Viva-Voce Examination of **Mr. M.JAYAPRAKASH** (Reg. No. 4081052109) on his Ph.D. Thesis Entitled "**CERTAIN INVESTIGATIONS ON PERFORMANCE ANALYSIS OF VLSI IMPLEMENTATION OF AREA EFFICIENT MULTIPLIER-ACCUMULATOR UNIT FOR FIR FILTER**" was conducted on 13-10-2014 at 3.00 PM in the ECE Department Seminar Hall, SNS College of Technology, Coimbatore - 641 035.



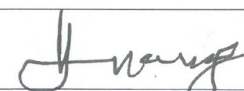
The following Members of the Oral Examination Board were present:

1.	Dr. SUDEB DASGUPTA Associate Professor, Department of Electronics & Communication Engineering, Indian Institute Technology, Roorkee - 247 667, Uttarakhand.	Indian Examiner
2.	Dr. C.VASANTHANAYAKI Associate Professor, Department of Electronics & Communication Engineering, Government College of Technology, Coimbatore - 641 035.	Subject Expert
3.	Dr. A.SHANMUGAM Dean & Professor, Department of Electronics and Communication Engineering, SNS College of Technology - 641 035.	Supervisor & Convener

The research scholar, **Mr. M.JAYAPRAKASH** presented the salient features of his Ph.D. work. This was followed by questions from the board members. The questions raised by the Foreign and Indian Examiners were also put to the scholar. The scholar answered the questions to the full satisfaction of the board members.

The corrections suggested by the Indian / Foreign examiners have been carried out and incorporated in the Thesis before Oral examination.

Based on the scholar's research work, his presentation and also the clarifications and answers by the scholar to the questions, the board recommends that **Mr.M.JAYAPRAKASH** be awarded **Ph.D. degree** in the Faculty of **INFORMATION AND COMMUNICATION ENGINEERING.**

1.	Dr. SUDEB DASGUPTA	Indian Examiner	
2.	Dr. C.VASANTHANAYAKI	Subject Expert	
3.	Dr. A.SHANMUGAM	Supervisor & Convener	

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Certified that this Thesis titled **“CERTAIN INVESTIGATIONS ON PERFORMANCE ANALYSIS OF VLSI IMPLEMENTATION OF AREA EFFICIENT MULTIPLIER-ACCUMULATOR UNIT FOR FIR FILTER”** is the bona fide work of **Mr. JAYAPRAKASH, M.** who carried out the research under my supervision. Certified further, that to the best of my knowledge the work reported herein does not form part of any other thesis or dissertation on the basis of which a degree or award was conferred on an earlier occasion on this or any other scholar.

I understand the University's policy on plagiarism and declare that the thesis and publications are my own work, except where specifically acknowledged and has not copied from other sources or been previously submitted for award or assessment.

Signature of the Scholar
Mr. M. JAYAPRAKASH

Place : Coimbatore
Date :

Signature of the Supervisor
Dr. A. SHANMUGAM
SUPERVISOR
Professor
Department of Electronics and
Communication Engineering,
SNS College of Technology,
Coimbatore-641035.