

## LIST OF PUBLICATIONS

### Publication in Journals

1. **Jayaprakash, M** & Shanmugam, A 2013, 'Design and Analysis of Low Power Hybrid Adder', Journal of Theoretical and applied Information Technology, ISSN 1992 – 8645, , Impact Factor - 1.71. Annexure II.
2. **Jayaprakash, M** & Shanmugam, A 2013, 'Modified Booth Encoding Radix-4 Multiplier', International Journal of Electronics and Communication Technology, ISSN : 2230-7109 (Online) vol. 4, no. 3, July-Sep.
3. **Jayaprakash, M** & Shanmugam, A 2013, 'Compound Adder Design Using Carry look ahead Adder / Carry Select Adders', International Journal of Research in Electronics & Communication Engineering, vol. 1, no. 2, pp. 56-61, October-December.
4. **Jayaprakash, M**, Peer Mohamed, M & Shanmugam, A 2013, 'Low Power and Area Efficient Multiplier for MAC', International Journal of Advanced Research in Electronics and Communication Engineering, ISSN: 2278 – 909X vol. 2, no. 11, November.
5. **Jayaprakash, M**, Abindas, PK & Barani Priyanga, 2013, 'An Efficient VLSI Design Approach to Reduce Static Powerusing Variable Body Biasing', International Journal of Electronics and Communication Technology, ISSN : 2230-7109 (Online) vol. 4, no. 2, Jan-March.
6. **Jayaprakash, M**, Peer Mohamed, M & Shanmugam, A 2013, 'Design and Analysis of Low Power and Area Efficient Multiplier' IJEMC.
7. **Jayaprakash, M**, Peer Mohamed, M & Shanmugam, A 2013, 'Low Power and High Speed MAC Unit Design Using Booth Multiplier and Carry Select Adder', IJARCS.

## International Conferences

1. **Jayaprakash, M & Shanmugam, A** 2013, 'Memory By-passing Scheme for Layered Decoding Architecture in LDPC Decoder', International conference on innovative Research in Engineering and Technology, Park College of Engineering and Technology, Jan.
2. **Jayaprakash, M & Shanmugam, A** 2010, 'Reduced Delay BCD adder', International conference on innovative Research in Engineering and Technology, Park College of Engineering and Technology, Aug.
3. **Jayaprakash, M** 2012, 'Building and IVR System for academic institutions by using IP04', International conference on 21-22 March 2012 at RVS College of Engineering and Technology, Coimbatore.
4. **Jayaprakash, M** 2011, 'VLSI based High speed packet data transfer', National Conference Computing, Communications and Devices, on 25<sup>th</sup> March 2011 at Adithya Institute of Technology, Coimbatore.
5. **Jayaprakash, M & Prem Anand, S** 2009, 'A programmable SIMD based multi-standard rake receiver architecture', national conference on Innovative Ideas in Mechatronics, 14<sup>th</sup> March 2009 at Kumaraguru College of Technology, Coimbatore.