

**CERTAIN INVESTIGATIONS ON  
PERFORMANCE ANALYSIS OF VLSI  
IMPLEMENTATION OF AREA EFFICIENT  
MULTIPLIER-ACCUMULATOR UNIT  
FOR FIR FILTER**

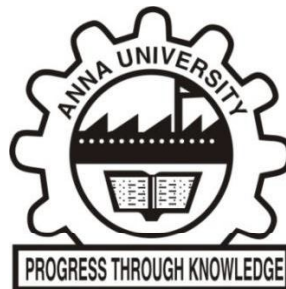
**A THESIS**

*Submitted by*

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*in partial fulfillment of the requirements for the degree of*

**DOCTOR OF PHILOSOPHY**



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