Figure A 1.1 SIMULINK block diagram of PI controller with reset wind up for a HWT
Figure A 1.2 SIMULINK block diagram of Fuzzy Assisted PI controller for a HWT
Figure A 1.3 SIMULINK block diagram of Backstepping Stabilizing controller for a HWT

Figure A 1.4 SIMULINK block diagram of Backstepping Stabilizing Subsystem
Figure A 1.5 SIMULINK block diagram of Backstepping Tracking controller for a HWT

Figure A 1.6 SIMULINK block diagram of Backstepping Tracking Subsystem
Figure A 1.7 SIMULINK block diagram of Backstepping Stabilizing controller with Fuzzy inference for a HWT

Figure A 1.8 SIMULINK block diagram of hwt_fuzzy subsystem
Figure A 1.9 SIMULINK block diagram of Backstepping Tracking controller with Fuzzy inference for a HWT

Figure A 1.10 SIMULINK block diagram of Single Stage controller with Fuzzy inference for a HWT
Figure A 1.11 SIMULINK block diagram of Two Stage controller with Fuzzy inference for a HWT

Figure A 1.12 SIMULINK block diagram of MRFPI controller for a HWT
Figure A 1.13 SIMULINK block diagram of MRFC controller for a HWT

Figure A 1.14 SIMULINK block diagram of Cascade subsystem
Figure A 1.15 SIMULINK block diagram of PI subsystem

Figure A 1.16 SIMULINK block diagram of PI subsystem
Figure A 1.17 SIMULINK block diagram of P2 subsystem