

**DESIGN OF NEW HIGH PERFORMANCE/LOW POWER
FULL SWING XOR/XNOR LOGIC GATES USING
MOSFET/FINFET/CNTFET TRANSISTORS**

A THESIS

submitted by

**SARADA MUSALA
(111PG05004)**

*for the award of the degree
of*

DOCTOR OF PHILOSOPHY

Under the guidance of

Dr. AVIRENI SRINIVASULU



VIGNAN'S

Foundation for Science, Technology & Research

(Deemed to be University)

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**DEPARTMENT OF ELECTRONICS AND COMMUNICATION
ENGINEERING**

**VIGNAN'S FOUNDATION FOR SCIENCE, TECHNOLOGY AND
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