# LIST OF ABBREVIATIONS

<table>
<thead>
<tr>
<th>Abbreviation</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>APBDRR</td>
<td>Advanced Priority Based Dynamic Round Robin</td>
</tr>
<tr>
<td>AAIRR</td>
<td>An Additional Improved Round Robin</td>
</tr>
<tr>
<td>ATS</td>
<td>Assumed Time Slice</td>
</tr>
<tr>
<td>ASMP</td>
<td>Asynchronous Multi-Core Processors</td>
</tr>
<tr>
<td>AR</td>
<td>Average Register</td>
</tr>
<tr>
<td>ATAT</td>
<td>Average Turnaround Time</td>
</tr>
<tr>
<td>AWT</td>
<td>Average Waiting Time</td>
</tr>
<tr>
<td>BJF</td>
<td>Best Job First</td>
</tr>
<tr>
<td>CTQ</td>
<td>Changeable Time Quantum</td>
</tr>
<tr>
<td>CC</td>
<td>Computed Component</td>
</tr>
<tr>
<td>CSC</td>
<td>Context Switch Component</td>
</tr>
<tr>
<td>DQRRR</td>
<td>Dynamic Quantum With Re-Adjusted Round Robin</td>
</tr>
<tr>
<td>ERR</td>
<td>Enhanced RR</td>
</tr>
<tr>
<td>FP</td>
<td>Factor Of Precedence</td>
</tr>
<tr>
<td>FSWT</td>
<td>Fair-Share Scheduling Algorithm With Weighted Time Slice</td>
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<tr>
<td>FCFS</td>
<td>First Come First Serve</td>
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<tr>
<td>FJFDRR</td>
<td>Fittest Job First Dynamic Round Robin</td>
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<tr>
<td>IMRRSjf</td>
<td>Improved Mean Round Robin With Shortest Job First</td>
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<tr>
<td>IMLFQ</td>
<td>Improved Multilevel Feedback Queue</td>
</tr>
<tr>
<td>IRR</td>
<td>Improved Round Robin</td>
</tr>
<tr>
<td>IRRVQ</td>
<td>Improved Round Robin Scheduling Algorithm With Varying Time Quantum</td>
</tr>
<tr>
<td>ISRBRR</td>
<td>Improved Shortest Remaining Burst Round Robin</td>
</tr>
<tr>
<td>IST</td>
<td>Intelligent Service Time</td>
</tr>
<tr>
<td>MMRR</td>
<td>Max Min Round Robin</td>
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<tr>
<td>MTQ</td>
<td>Median Time Quantum</td>
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<tr>
<td>MDTQRR</td>
<td>Multi-Dynamic Time Quantum Round Robin</td>
</tr>
<tr>
<td>MLFQ</td>
<td>Multilevel Feedback Queue</td>
</tr>
<tr>
<td>MLQ</td>
<td>Multilevel Queue</td>
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<tr>
<td>MPSoC</td>
<td>Multi-Processor System On Chip</td>
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<tr>
<td>NCS</td>
<td>Number Of Context Switches</td>
</tr>
<tr>
<td>Abbreviation</td>
<td>Description</td>
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<td>--------------------------------------------------</td>
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<tr>
<td>OTS</td>
<td>Original Time Slice</td>
</tr>
<tr>
<td>PSIST</td>
<td>Precedence Scheduling Algorithm With Intelligent Service Time</td>
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<tr>
<td>PBDRR</td>
<td>Priority Based Dynamic Round Robin</td>
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<tr>
<td>PBSRR</td>
<td>Priority Based Static Round Robin</td>
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<tr>
<td>PC</td>
<td>Priority Component</td>
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<tr>
<td>PCB</td>
<td>Process Control Block</td>
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<tr>
<td>RR</td>
<td>Round Robin</td>
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<tr>
<td>SARR</td>
<td>Self Adjustment RR</td>
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<tr>
<td>SEFDRR</td>
<td>Shortest Execution First Dynamic Round Robin</td>
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<tr>
<td>SJF</td>
<td>Shortest Job First</td>
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<tr>
<td>SRBRR</td>
<td>Shortest Remaining Burst Round Robin</td>
</tr>
<tr>
<td>SRTN</td>
<td>Shortest Remaining Time Next</td>
</tr>
<tr>
<td>SC</td>
<td>Shortness Component</td>
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<tr>
<td>SJFDRR</td>
<td>Smart Job First Dynamic Round Robin</td>
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<tr>
<td>SJFMLFQ</td>
<td>Smart Job First Multilevel Feedback Queue</td>
</tr>
<tr>
<td>SORR</td>
<td>Smart Optimized Round Robin</td>
</tr>
<tr>
<td>SPF</td>
<td>Smart Priority Factor</td>
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<tr>
<td>STQ</td>
<td>Smart Time Quantum</td>
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<tr>
<td>SR</td>
<td>Store Register</td>
</tr>
<tr>
<td>SMP</td>
<td>Synchronous Multi-Core Processors</td>
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<tr>
<td>TPBCS</td>
<td>Two Processor Based CPU Scheduling</td>
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<tr>
<td>UTQ</td>
<td>Upper Quartile Time Quantum</td>
</tr>
<tr>
<td>WTS</td>
<td>Weighted Time Slice</td>
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