DECLARATION

I hereby declare that the entire work embodied in the thesis entitled, “Design and Implementation of Hardware Accelerators for Image Processing Algorithms”, is the result of investigation carried out by me at the Department of Electronics, Banasthali Vidyapith, Bansasthali under the supervision of Dr. Seema Verma, Professor, Department of Electronics, Banasthali Vidyapith, Bansasthali, Rajasthan, India.

I also declare that it has not been submitted elsewhere for any degree or diploma. In keeping with the general practice, due acknowledgements have been made whenever the work described is based on finding of other investigators. Any omissions that might have occurred due to oversight or error in judgment are regretted.

Date: Megha Sharma