

CERTIFICATE

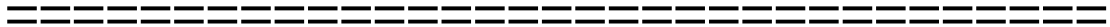
This is to certify that the thesis entitled "**Design of Low Power High Speed Static Random Access Memory (SRAM) Using Floating Gate MOSFET**" submitted by Kananbala Ray, a Research Scholar in the School of Electronics Engineering, KIIT University, Bhubaneswar, India, for the award of the degree of Doctor of Philosophy, is a record of an original research work carried out by her under my supervision and guidance. The thesis fulfils all requirements as per the regulations of the University and in my opinion, has reached the standard needed for submission. To the best of my knowledge neither this thesis nor any part of it has been submitted for any degree or any academic award elsewhere.

Dr. Sushanta Kumar Mandal

Supervisor

Department of Electronics and Communication Engineering

Centurion University of Technology and Management, Bhubaneswar



The Ph.D Viva-voce examination of **Kananbala Ray**, Research Scholar, has been held on and is recommended for the award of the Ph.D Degree.

Dr. Sushanta Kumar Mandal

Supervisor

CUTM, BBSR

External Examiner