

DECLARATION

I hereby declare that the work which is being presented in the thesis entitled “**Design Of Low Power High Speed Static Random Access Memory (SRAM) Using Floating Gate MOSFET**” in fulfilment of the requirements for the award of degree of Doctor of Philosophy in Faculty of Electronics and Telecommunication Engineering and submitted to KIIT University, Bhubaneswar is an authentic record of my own work carried out during the period from September 2012 to July 2017 under the supervision of Dr. Sushanta Kumar Mandal, Department of Electronics and Communication Engineering, Centurion University of Technology and Management, Bhubaneswar.

The matter embodied in this thesis has not been submitted by me for the award of any other degree of this or any other University/Institute.

Kananbala Ray

This is to certify that above statement made by the scholar is correct to the best of my knowledge.

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