DECLARATION

I hereby declare that the work described in this thesis, entitled “Coprocessor Design for Split Step Fourier Method” which is being submitted by me in partial fulfillment for the award of Doctor of Philosophy (Ph.D.) in the Department of Electronics and Communication to the Jawaharlal Nehru Technological University Hyderabad, Kukatpally, Hyderabad (A.P.) -500 085, is the result of investigations carried out by me under the Guidance of Dr. R. M. Banakar, Professor and Head of Electronics and Communication Department, BVBCET, Hubli and Dr. B. C. Jinaga, Professor (RTD) Electronics and Communication Department, JNTU, Hyderabad.

The work is original and has not been submitted for any Degree/Diploma of this or any other university.

Place: Hubli

Date: ___________________________  Signature: ___________________________

Name of the Candidate: Saroja V. Siddamal

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DECEMBER 2012