ACKNOWLEDGEMENT

I express my regards and sense of gratitude to my supervisor, Prof. Basant Kumar Mohanty for giving inspiration, expert guidance, moral support and encouragement throughout this work. He has provided me with an environment conducive to learning and quality research. Perhaps, most of all, I would like to thank Prof. Basant Kumar Mohanty for always having confidence in me and my abilities.

I am very much grateful to the Prof. D. S. Hooda, Dean (Research) for his indispensable support and motivation throughout the research. I also want to acknowledge Prof. Rajiv Saxena, HOD (ECE) for his priceless advices and support. I want to thank Prof. K. K. Jain, Dean (Academics) for his well wishes and kind support during my research work. I am very much indebted to the honorable Prof. N. J. Rao, Vice Chancellor and Dr. Y. Medury, COO, Jaypee Education System, for providing me the opportunity to carry out quality research.

I would like to express my special thanks to Dr. P.K. Meher (Senior Research Scientist, NTU, Singapore) for his valuable suggestions. I would like to express my gratitude to Dr. Arnav Ray, Dr. Baliram Gupta, Mr. Subhod Singhal and Mr. Sujit Patel for their valuable support. I would like to express my heartiest thanks to Dr. Tirupathiraju Kanumuri, Dr. Ravi Kumar, Mr. Gaurav Saxena, Ms. Shefali Sharma, Mr. Vikas Tiwari and Mr. Sunil Dutt Sharma for their support. I would like to acknowledge Jaypee University of Engineering & Technology for providing a platform and the financial support, without which it would not be possible to do the research. I would like to thank Mr. Ajay Ranjan Lab staff of VLSI Lab for his kind help. I also want to thank the people who always motivated and supported me directly or indirectly during my Ph.D. work.

At last I want to express my gratitude to the almighty God, my parents, parents-in-law, my brother Sanjay, my wife Reeta and my children Ishika and Anay for motivating and supporting me in every step of life.

Anurag Mahajan