

LIST OF ABBREVIATIONS

A ² BC	-	Adaptive Address Bus Coding
ACCS	-	Adaptive Cross Connection Scheme
ASIC	-	Application specific integrated circuit
ALU	-	Arithmetic Logic Unit
BCD	-	Binary Coded Decimal
BEC	-	Binary to Excess-1 converter
BPC	-	Bits per character
BI	-	Bus Invert
CLA	-	Carry Look ahead adder
CS	-	Carry Select
CSA	-	Carry Select adder
CLS	-	Circular left shift
CRS	-	Circular right shift
CMOS	-	Complementary metal oxide semiconductor
CPL	-	Complementary Pass Transistor
CSL	-	Conventional static CMOS Logic
Cu	-	Copper
CT	-	Coupling Transition
CT	-	Coupling Transition
DSM	-	Deep Submicron
DCVS	-	Differential cascade voltage swing
DCVSPG	-	Differential Cascade Voltage Swing Pass gate
DCT	-	Discrete cosine Transform
DPL	-	Double Pass Transistor Logic
DRDL	-	Dual Rail Domino Logic
DDCVSL	-	Dynamic differential cascode voltage switch logic

EDA	Electronic design Automation
ECL	- Emitter coupled Logic
EEPL	- Energy Economized Pass Transistor Logic
XOR	- Exclusive OR Logic
FPGA	- Field programmable gate array
FDTD	- Finite difference time domain
GaAs	- Gallium Arsenide
HD	- Hamming distance
HDL	- Hardware description Language
Inc –xor	- Incremental xor
I/O	- Input / Output
IC	- Integrated circuit
ITRS	- International Technology Road map for Semiconductors
IEL	- Invert even line
IOL	- Invert odd line
JPEG	- Joint Photographic experts group
LEAP	- Lean Integration with Pass transistors
LZ	- Lempel Ziv
LZW	- Lempel Ziv Welch
LSIS	- Locally Spatial Invert Scheme
MOSFET	- Metal oxide semiconductor field effect transistor
MPEG	- Motion picture experts group
MCT	- Multi coding Technique
MPBI	- Multiway Partial Bus Invert
NOC	- Network on chip
NBCMEI	- Novel bus coding method invert even
NDSMBC	- Novel Deep Sub micron Bus coding
NMOS	- N-Type metal oxide semiconductor
OLZWH	- Optimality of Lempel Ziv Welch & adaptive Huffman

PBI	-	Partial Bus Invert
PTL	-	Pass Transistor Logic
PDP	-	Power delay product
PSSL	-	Preset Skewed Static Logic
Xor-pbm	-	Probability based mapping xor
PMOS	-	P-Type metal oxide semiconductor
RISC	-	Reduced instruction set computer
RTL	-	Register Transistor Logic
RC Model	-	Resistance capacitance model
RLC	-	Resistance Inductance Capacitance
RCA	-	Ripple carry adder
RLI	-	Rotate left with invert
RRI	-	Rotate right with invert
RLE	-	Run Length Encoding
ST	-	Self Transition
ST	-	Self Transition
SMS	-	Short message service
SBC	-	Simple byte compression
SBD	-	Simple byte decompression
SQRT CSLA	-	Square root Carry select adder
SDCVSL	-	Static differential cascode voltage switch Logic
SDSL	-	Static differential split level
SRPL	-	Swing Restored Pass transistor Logic
SOC	-	System on chip
3D	-	Three dimension topology
TTL	-	Transistor-transistor Logic
TPC	-	Transition Pattern Coding
2D	-	Two dimension
VQ	-	Vector Quantization

- VHDL - Very high speed hardware description language
- VLSI - Very Large Scale Integration
- WCM - Weighted Code Mapping
- XPower - Xilinx Power