



ANNA UNIVERSITY, CHENNAI - 600 025  
CENTRE FOR RESEARCH



CERTIFICATE

1. This is to certify that no corrections/suggestions were pointed out by the Indian/Foreign Examiner(s) in the Thesis titled " MINIMIZING POWER DISSIPATION THROUGH TRANSITION ACTIVITY IN VLSI INTERCONNECTS " submitted by Mr./Ms. Vithyalakshmi.N

OR

2. This is to certify that all corrections and suggestions pointed out by the Indian /Foreign Examiner(s) are incorporated in the Thesis titled " MINIMIZING POWER DISSIPATION THROUGH TRANSITION ACTIVITY IN VLSI INTERCONNECTS " submitted by Mr./Ms. Vithyalakshmi.N

Signature of the Joint Supervisor  
(if applicable)

Signature of the Supervisor

Place :

Date :





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Proceedings of the Ph.D. Viva-Voce Examination of Mr./Ms. Vithyalakshmi.N held at on in

The Ph.D. Viva-Voce Examination of Mr./Ms. Vithyalakshmi.N (Reg. No. 2009740132) on his/her Thesis Entitled " MINIMIZING POWER DISSIPATION THROUGH TRANSITION ACTIVITY IN VLSI INTERCONNECTS " was conducted on at in the .

The following Members of the Oral Examination Board were present:

- |  |                       |
|--|-----------------------|
| 1. Dr. B Ramadoss, Professor, Department of Computer Applications, National Institute of Technology, Tiruchirappalli                             | Indian Examiner       |
| 2. Dr. Indra Getzy David, Professor & Head, Department of Electrical and Electronics Engineering, Government College of Engineering, Tirunelveli | Subject Expert        |
| 3. Dr. Rajaram.M, Vice-Chancellor, Chennai-600 025.,   | Supervisor & Convenor |

The research scholar, Mr./Ms. Vithyalakshmi.N presented the salient features of his/her work. This was followed by questions from the board members. The questions raised by the Foreign and Indian Examiners were also put to the scholar. The scholar answered the questions to the full satisfaction of the board members.

The corrections suggested by the Indian/Foreign examiner have been carried out and incorporated in the Thesis before the Oral examination.

Based on the scholars research work, his/her presentation and also the clarifications and answers by the scholar to the questions, the board recommends that Mr./Ms.Vithyalakshmi.N be awarded Ph.D. degree in the **Faculty of** .

- |                            |  |                           |
|----------------------------|--|---------------------------|
| 1. DR. B. RAMADOSS         | <i>B. Ramadoss</i>                         | Indian Examiner           |
| 2. DR. INDRA GETZY DAVID   | <i>Dr. Indra Getzy David</i><br>13/11/2015 | Subject Expert            |
| 3. <i>(DR. M. RAJARAM)</i> | <i>Dr. M. Rajaram</i><br>13/11/2015        | Supervisor & Convenor     |
| 4. —                       |  | Joint Supervisor (if any) |



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The research work embodied in the present Thesis entitled “**MINIMIZING POWER DISSIPATION THROUGH TRANSITION ACTIVITY IN VLSI INTERCONNECTS**” has been carried out in the Department of Electrical and Electronics Engineering, Anna University Chennai. The work reported herein is original and does not form part of any other thesis or dissertation on the basis of which a degree or award was conferred on an earlier occasion or to any other scholar.

I understand the University’s policy on plagiarism and declare that the thesis and publications are my own work, except where specifically acknowledged and has not been copied from other sources or been previously submitted for award or assessment.

**VITHYALAKSHMI N**  
RESEARCH SCHOLAR

  
**Dr. M. RAJARAM**

SUPERVISOR

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