

LIST OF PUBLICATIONS

International Journals

1. **Vithyalakshmi, N & Rajaram, M** 2014, 'A multi coding technique to reduce transition activity in VLSI circuits', Journal of Semiconductors, vol. 35, no. 2, pp. 1- 6, ISSN : 1674-4926,cited by 1 (**Annexure II**).
2. **Vithyalakshmi, N & Rajaram, M** 2014, 'Minimizing area and Power dissipation in Low power VLSI circuits using Complementary Pass Transistor Logic', International journal of Applied Engineering Research, vol. 9, no. 26, pp. 9408-9412, ISSN : 0973-4562 (**Annexure II**).
3. **Vithyalakshmi, N & Rajaram, M** 2013, 'An Octo Coding technique to reduce energy transition in low power VLSI circuits', International journal of Research in Engineering and Technology, vol. 2, issue 11, pp. 674-679, ISSN : 2319-1163 .

International Conferences

1. **Vithyalakshmi, N & Rajaram, M** 2015, 'Data compression and simple encoding technique to reduce transition activity & power dissipation in VLSI circuits', Bilingual International Conference on information technology, DESIDOC, DRDO, Metcalfe House, Delhi, no. 6, pp. 261-265.

