

**MINIMIZING POWER DISSIPATION THROUGH
TRANSITION ACTIVITY IN VLSI
INTERCONNECTS**

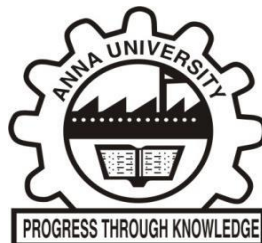
A THESIS

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