



ANNA UNIVERSITY, CHENNAI - 600 025  
CENTRE FOR RESEARCH



CERTIFICATE

1. This is to certify that no corrections/suggestions were pointed out by the Indian/Foreign Examiner(s) in the Thesis titled " ANALYSIS OF SWITCH FAULT OF CASCADED H-BRIDGE MULTILEVEL INVERTER USING SIGNAL PROCESSING AND SOFT COMPUTING TECHNIQUES " submitted by Mr./Ms. Sivakumar.M

OR

2. This is to certify that all corrections and suggestions pointed out by the Indian /Foreign Examiner(s) are incorporated in the Thesis titled " ANALYSIS OF SWITCH FAULT OF CASCADED H-BRIDGE MULTILEVEL INVERTER USING SIGNAL PROCESSING AND SOFT COMPUTING TECHNIQUES " submitted by Mr./Ms. Sivakumar.M

Signature of the Joint Supervisor  
(if applicable)

Signature of the Supervisor

**Dr. R.M.S. PARVATHI, B.E.(ECE), M.E.(CSE), Ph.D.,**  
**Professor,**  
**Sri Ramakrishna Institute of Technology,**  
**Coimbatore - 641 010.**

Place :

Date :





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Proceedings of the Ph.D. Viva-Voce Examination of Mr./Ms. Sivakumar.M held at 10:30 AM on 10.08.2015 in Sri Ramakrishna Institute of Technology Perur -Pachapalayam Coimbatore.

The Ph.D. Viva-Voce Examination of Mr./Ms. Sivakumar.M (Reg. No. 4086042125) on his/her Ph.D. Thesis Entitled "ANALYSIS OF SWITCH FAULT OF CASCADED H-BRIDGE MULTILEVEL INVERTER USING SIGNAL PROCESSING AND SOFT COMPUTING TECHNIQUES " was conducted on 10.08.2015 at 10:30 AM in the Sri Ramakrishna Institute of Technology Perur -Pachapalayam Coimbatore..

The following Members of the Oral Examination Board were present:

- |   |                       |
|---|-----------------------|
| 1. Dr. T Devaraju, Professor, Department of Electrical and Electronics Engineering, Sree Vidyanikethan Engineering College, Tirupati      | Indian Examiner       |
| 2. Dr. N. Devarajan, Professor & Head, Department of Electrical and Electronics Engineering, Government College of Technology, Coimbatore | Subject Expert        |
| 3. Dr. Parvathi. R. M. S., Professor & Head, Sri Ramakrishna Institute of Technology, Coimbatore  | Supervisor & Convenor |

The research scholar, Mr./Ms. Sivakumar.M presented the salient features of his/her Ph.D. work. This was followed by questions from the board members. The questions raised by the Foreign and Indian Examiners were also put to the scholar. The scholar answered the questions to the full satisfaction of the board members.

The corrections suggested by the Indian/Foreign examiner have been carried out and incorporated in the Thesis before the Oral examination.

Based on the scholars research work, his/her presentation and also the clarifications and answers by the scholar to the questions, the board recommends that Mr./Ms. Sivakumar.M be awarded Ph.D. degree in the Faculty of Electrical Engineering.

- |                          |                            |                           |
|--------------------------|----------------------------|---------------------------|
| 1. Dr. T. Devaraju       | <i>Dwan</i>                | Indian Examiner           |
| 2. Dr. N. Devarajan      | <i>LLR</i>                 | Subject Expert            |
| 3. Dr. R. M. S. Parvathi | <i>Dr. R.M.S. Parvathi</i> | Supervisor & Convenor     |
| 4. _____                 |                            | Joint Supervisor (If any) |

**Dr. R.M.S. PARVATHI, B.E.(ECE), M.E.(CSE), Ph.D.,**  
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**CERTIFICATE**

The research work embodied in the present Thesis entitled “**ANALYSIS OF SWITCH FAULT OF CASCADED H-BRIDGE MULTILEVEL INVERTER USING SIGNAL PROCESSING AND SOFT COMPUTING TECHNIQUES**” has been carried out in the Department of Electrical and Electronics Engineering, Sri Ramakrishna Institute of Technology, Coimbatore. The work reported herein is original and does not form part of any other thesis or dissertation on the basis of which a degree or award was conferred on an earlier occasion or to any other scholar.

I understand the University's policy on plagiarism and declare that the thesis and publications are my own work, except where specifically acknowledged and has not been copied from other sources or been previously submitted for award or assessment.

**SIVAKUMAR M**  
Research Scholar

Counter Signed by

  
**Dr. R. M. S. Parvathi**

**SUPERVISOR**

Professor,

Department of Electronics and

Communication Engineering,

Sri Ramakrishna Institute of

Technology, Coimbatore - 641 010

**Dr. R.M.S. PARVATHI, B.E.(ECE), M.E.(CSE), Ph.D.**  
**Professor**  
Sri Ramakrishna Institute of Technology  
Coimbatore - 641 010.