

Date: 12.12.2011

CERTIFICATE

This is to certify that all corrections and suggestions pointed out by the Indian and foreign Examiners are incorporated in the thesis titled “**AREA EFFICIENT RECONFIGURABLE RESOURCE SHARING ARCHITECTURE FOR FUTURE WIRELESS COMMUNICATIONS**” submitted by **Mr.T.SURESH**, Ph.D. Scholar (Reg. No.2713499709)



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Place: Kavaraipettai

Date: 12.12.2011

Ph.D.	PT
Reg. No.	2713499709

**PROCEEDINGS OF THE Ph.D VIVA-VOCE EXAMINATION OF Mr.T.SURESH
HELD AT 10.30 A.M ON 12-12-2011 IN R.M.K Engineering College, Kavaraipettai -601 206**

The Ph.D. Viva-Voce Examination of Mr. T.SURESH (Reg. No. 2713499709) on his Ph.D. Thesis Entitled "AREA EFFICIENT RECONFIGURABLE RESOURCE SHARING ARCHITECTURE FOR FUTURE WIRELESS COMMUNICATIONS " was conducted on 12-12-2011 at 10.30 A.M. in the Department of Computer Science And Engineering, R.M.K Engineering College , Kavaraipettai - 601 206.




The following Members of the Oral Examination Board were present:


1. Dr.S.Narayana Reddy Indian Examiner
Professor, Dept. of Electronics & Communication Engineering,
College of Engg., Sri Venkateswara University,
Tirupati - 517 502, A.P
2. Dr.Ranjani Parthasarathy Expert Member
Professor, Dept. of Computer Science & Engineering,
Anna University, Chennai - 600 025
3. Dr.K.L.Shunmuganathan Supervisor & Convener
Professor & Head, Dept. of Computer Science & Engg.,
R.M.K Engineering College, Kavaraipettai - 601 206

The research scholar, Mr. T.SURESH presented the salient features of his Ph.D. work. This was followed by questions from the board members. The questions raised by the Foreign and Indian Examiners were also put to the scholar. The scholar answered the questions to the full satisfaction of the board members.

The corrections suggested by the Indian/Foreign examiner have been carried out and incorporated in the Thesis before the Oral examination.

Based on the scholar's research work, his presentation and also the clarifications and answers by the scholar to the questions, the board recommends that Mr.T.SURESH be awarded Ph.D. degree in the Faculty of I&C.

1.  Indian Examiner
2.  Expert Member
3.  Supervisor & Convener

**INFORMATION AND COMMUNICATION
ENGINEERING**

C SUPERVISOR

ANNA UNIVERSITY
CHENNAI 600 025

BONAFIDE CERTIFICATE

Certified that this Thesis titled, “**AREA EFFICIENT RECONFIGURABLE RESOURCE SHARING ARCHITECTURE FOR FUTURE WIRELESS COMMUNICATIONS**” is the bonafide work of **Mr. SURESH, T.** who carried out the research under my supervision. Certified further, that to the best of my knowledge the work reported herein does not form part of any other thesis or dissertation on the basis of which a degree or award was conferred on an earlier occasion on this or any other scholar.

Place : Kavaraipeetai

Date :

Dr. K.L. SHUNMUGANATHAN

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