PICO: An Ultra lightweight and Low power encryption design for pervasive computing

Gaurav Bansod, Narayan Pisharoty, and Abhijit Patil

Abstract— In this paper we are proposing an ultra lightweight, a very compact block cipher ‘PICO’. PICO is a substitution and permutation based network, which operates on a 64 bit plain text and supports a key length of 128 bits. It has a VERY compact structure that requires GEs for a 128 bit key length. The PICO cipher uses strong bit permutation layer which only needs wires for implementation this reduces overall gate count. Its unique design helps to generate a large number of active S - boxes in fewer rounds which thwart the linear and differential attacks on the cipher. PICO shows good performance on both the hardware and the software platforms. PICO consumes only 2504 bytes of Flash memory which is less than ultra lightweight cipher PRESENT. PICO has a very strong Substitution layer(S- box) which not only makes the design robust but also introduces a great avalanche effect. PICO has strong and compact key scheduling which is motivated from latest NSA designed cipher SPECK. PICO consumes 28mW of dynamic power which is less than the PRESENT cipher (31mW). In this paper we have presented the security analysis of PICO and its performance as an ultra lightweight compact cipher. PICO resists linear, differential, biclique, zero correlation, meet in the middle and key related attacks.

Index Terms— Lightweight Cryptography, SP Network, Block cipher, IoT, Encryption, Embedded security

I. INTRODUCTION
In recent years, many lightweight ciphers are introduced which has less footprint area, low power consumption and less gate counts. Lightweight Ciphers like PRESENT [1], PICCOLO [2], TWINE [3], SIMON and SPECK [4] have the robust design and needs less than 2200 Gate Equivalents (GEs) for implementation. Recently NSA launched the SIMON and SPECK cipher which are considered to be the most ultra lightweight ciphers. SIMON and SPECK has robust design and interesting key scheduling which makes the design robust and can thwart all possible types of attacks. PRESENT being the S-P network also has compact design and its hardware implementation needs around 1800-2000 GEs. Cipher like PRESENT is design by keeping goal as compact hardware implementation. But PRESENT has very weak substitution layer and its permutation layer is also vulnerable to attacks like statistical Saturational attack [5]. PRESENT has bit permutations as its P-layer which only requires wires for its hardware implementation [1]. In this paper we aimed at design compact and robust S-P network cipher which not only needs less footprint area but also take care of the other factors like power consumption, GEs and all possible types of attacks.

In this paper we have presented a compact cipher called PICO which is S-P network that needs less GEs, less footprint area and low power consumption as compared to the PRESENT cipher. We also aimed at providing stronger substitution layer that makes the design more robust. PICO shows good resistance against linear and differential attacks. Pico also shows good resistance against biclique attacks, zero correlation attack and key related attack. We believe that the PICO is the most ultra lightweight S-P network cipher till date which has compact structure, robust design and needs less GEs for its hardware implementation.

We have used the following notation in this paper for PICO cipher

- \( P_j \) Input plaintext block of \( j = 64 \) bits
- \( C_j \) Output cipher text block of \( j = 64 \) bits
- \( K_i \) 64-bit subkey for round \( i \)
- \( \oplus \) Bitwise exclusive-OR operation
- \( \odot \) Bitwise AND operation
- \( \oplus \) Bitwise NOT operation
- \( \& \) Bitwise AND operation
- \( \| \) Concatenation of two strings
- \( \odot \) Bitwise OR operation

II. THE PICO BLOCK CIPHER
The design of PICO cipher is based on a substitution permutation network [6]. It has total 32 rounds. PICO cipher supports 64 bit plaintext and 128 bit key length. Fig. 1 shows the block diagram of PICO cipher. Plaintext bits/ cipher text bits are arranged in 4×16 array format as shown in Fig. 2(a).

Let \( P = p^3||\cdots||p^1||p^0 \) is the 64-bit plaintext, then row 0 contains the first 16 bits of plaintext \( p^3||\cdots||p^1||p^0 \), row 1 contains the next 16 bits \( p^3||\cdots||p^1||p^0 \), and so on, as shown in Fig. 2(a).

Fig. 2(b) represents the ‘Two-dimensional Representation’ of 4×16 array. Each round consist the following 3 steps:
AddRoundkey, 
SubColumn & 
Bit_Shuffle

After the last round, there is an AddRoundKey as shown in Fig 1.

![Fig. 1. Block Diagram of PICO cipher](image)

S-box is a nonlinear element in cipher design which is followed by a bit permutation layer. 33 different subkeys each of 64 bits is generated from the 128 bit user defined key bits followed by a bit permutation layer. 33 different subkeys each is the subkey which is used as post whitening key.

Pseudo code for PICO cipher is given as

\[ P = P^{63} \ldots P^0 \]

\[ \text{RoundKey}() \]

\[ \text{for} \ i = 0 \text{ to } 31 \text{ do} \]

\[ \text{Add_round_key} (P, K_i) \]

\[ \text{SubColumn} (A) \]

\[ \text{Bit_Shuffle} (A) \]

\[ \text{End} \text{ for} \]

\[ \text{Add_round_key} (A, K_{32}) \]

\[ C \rightarrow A \]

\[ \begin{bmatrix}
    p_{15} & p_{14} & \ldots & p_{1} & p_{0} \\
    31 & p_{30} & \ldots & p_{17} & p_{16} \\
    47 & p_{46} & \ldots & p_{33} & p_{32} \\
    63 & p_{62} & \ldots & p_{49} & p_{48}
\end{bmatrix} \]

\[ \begin{bmatrix}
    p_{15} & p_{14} & \ldots & p_{1} & p_{0} \\
    15 & p_{14} & \ldots & p_{1} & p_{0} \\
    2 & p_{14} & \ldots & p_{1} & p_{0} \\
    3 & p_{14} & \ldots & p_{1} & p_{0}
\end{bmatrix} \]

\[ \begin{bmatrix}
    q_{10} & q_{9} & \ldots & q_{1} & q_{0} \\
    10 & q_{9} & \ldots & q_{1} & q_{0} \\
    9 & q_{9} & \ldots & q_{1} & q_{0} \\
    8 & q_{9} & \ldots & q_{1} & q_{0}
\end{bmatrix} \]

Fig. 2 (a) represents 4×16 array format and its Two-dimensional representation is in (b)

Each round of PICO cipher consists of the following operations:

A. Add_round_key

Add_round_key performs an XOR operation with 64 bit plaintext and 64 bit sub key. Sub keys are denoted by \( K_i \) where \( i \) range from 0 to 32 and the current state output \( P \rightarrow p^{63} \ldots p^0 \) is given as,

\[ P \rightarrow P \oplus K_i \]

B. SubColumn

The S-box used in PICO cipher is of 4×4 S-box where S-box: \( F_2^4 \rightarrow F_2^4 \). Table 1 represents the hexadecimal values for the Substitution layer,

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<th>C</th>
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<td>F</td>
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<td>3</td>
<td>9</td>
<td>C</td>
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</tbody>
</table>

SubColumn operation is based on the simultaneous application of S-boxes to the 4 bits in same column. Operation of SubColumn is illustrated in Fig. 3. The input of a S-box is Column \((i) = p^{33} \parallel p^{31} \parallel p^{29} \parallel p^{0} \) where \( i \) ranges from 0 \( \leq i \leq 15 \) and \( p^0 \) is LSB bit and \( p^{33} \) is the MSB bit of the 4 bit nibble. The output is S-box(Column(i)) = \( q^{31} \parallel q^{29} \parallel q^{27} \parallel q^{0} \). S-box is applied column wise in our cipher design.

Let \( X = ABCD \) be the input to the S-box and \( Y = y_3y_2y_1y_0 \) is the output. For example \( X = ABCD = 0000 \) then \( Y = 1110 \).

\[ \begin{bmatrix}
    p_{15} & p_{14} & \ldots & p_{1} & p_{0} \\
    p_{14} & p_{13} & \ldots & p_{1} & p_{0} \\
    p_{13} & p_{12} & \ldots & p_{1} & p_{0} \\
    p_{12} & p_{11} & \ldots & p_{1} & p_{0}
\end{bmatrix} \]

\[ \begin{bmatrix}
    q_{10} & q_{9} & \ldots & q_{1} & q_{0} \\
    q_{9} & q_{8} & \ldots & q_{1} & q_{0} \\
    q_{8} & q_{7} & \ldots & q_{1} & q_{0} \\
    q_{7} & q_{6} & \ldots & q_{1} & q_{0}
\end{bmatrix} \]

Fig. 3. SubColumn Operation

C. Permutation_Layer

Permutation layer of PICO cipher is based on bit permutation and it is a linear permutation operation. The bit permutation used in PICO is given in the Table 2. The bit \( p^{ji} \) present in \( i \)th row and \( j \)th column is shifted to the new bit position as described in the Table 2. Bit_Shuffle(p) i.e. Bit_Shuffle(p\( ^{00} \)) \( \rightarrow p^{010} \). It means after permutation operation, the bit will be shifted to 0th row and 10th column.
TABLE 2
P-BOX OF PICO CIPHER

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<td>3,1</td>
<td>1,15</td>
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</table>

D. Key Schedule of 128-bit key length

Key schedule of PICO cipher is motivated from the SPECK cipher key scheduling design [4]. SPECK key scheduling is compact in memory size requirement and no attacks till date are reported on it.

In PICO cipher total 33 subkeys are used each of size 64 bits are extracted from 128 bit key scheduling algorithm which is mentioned in the next subsection.

1) 128-bit key scheduling

User defined 128 bit key is stored in the register Key, subkey K^0 and L^1 can be given as

Key = k^{127} \oplus k^{126} \oplus \ldots k^2 \oplus k^1 \oplus k^0
K^0 = k^{63} \oplus k^{62} \oplus \ldots k^1 \oplus k^0
L^1 = k^{127} \oplus k^{126} \oplus \ldots k^{66} \oplus k^{65} \oplus k^{64}

After extracting key as K^0 and L^1 each of 64 bits, the subkeys K^j to K^{15} are generated as follows

For j = 0 to 31 do
L^{j+1} = ((K^j_{64} \oplus RCS(L^j_{64,3})) \oplus (L^j_{1}));
K^{j+1}_{64} = ((L^j_{64} \oplus LCS(K^j_{64,7})) \oplus j;
L^{j+1}_{64} = L^j_{64};
End for

Where LCS represents left circular shift by 7 bits and RCS represents right Circular shift by 3 bits.

Subkeys are arranged in 4x16 array format as shown in Fig. 4 to perform AddRoundKey operation.

![Fig. 4. 4x16 two dimensional representations of Subkey bits](image_url)

III. SECURITY ANALYSIS OF PICO

Various cryptanalysis techniques are applied on a cipher to find robustness of cipher design against all possible types of attacks. In paper we have focused on attacks like the differential attack, the linear attack, the algebraic attack, the key scheduled attack, the key collision attack, the zero-correlation attack and the biclique attack. S-box is a nonlinear layer in cipher design and it plays a very important role to provide security against well-known attacks like linear attack and differential attack.

Design of a cipher is an important factor to increase large number of active S-boxes. Structure that provides large number of active S-boxes considered having robust architecture which can thwart all possible types of attacks. Computer based techniques are used in this paper for selection of good S-box and also to calculate the minimum number of active S-boxes.

A. Design Criteria of the S-box

Gate count is increased by using separate S-box in each round of cipher and similarly this does not provide sensitive amount of improvement in the resistance against known attacks [7]. We have chosen 4x4 S-box in our design of PICO cipher. S-box used in the PICO cipher is robust and prevents clustering of linear and differential trails. One of the most important aspect in our cipher design is the nonlinear robust layer i.e. S-box.

4x4 S-box provides compactness and selection of proper S-box provide resistance against linear and differential attack. These two parameters we have considered while designing the S-box.

PICO S-box is S: F_4 \rightarrow F_4, it means that it takes a 4 bit input and produces a 4 bit output. Important properties for a good S-box design are mentioned below which we have considered for S-box selection.

Property 1: Linear Property

A \in F_4 is input to the S-box. A is the input mask and B is the output mask such that A \in F_4, B \in F_4 so LC (A, B) is defined as:

LC (A, B) = \# \{ a \in F_4 \mid A \oplus a = B \oplus S(a) \} - 8

This property is used to form the Linear Approximation Table (LAT), where LC represents Linear Cryptanalysis [8].The • denotes mask operation on F_4 and # indicate the number of matches in LAT for the input mask A and B minus 8.

Property 2: Differential Property

A \in F_4 is input to the S-box and \Delta A, \Delta B are the input and output differences such that \Delta A, \Delta B \in F_4 so that DC (\Delta A, \Delta B) is defined as:

DC (\Delta A, \Delta B) = \# \{ a \in F_4 \mid S(a) \oplus S(a \oplus \Delta A) = \Delta B \}

This property used to form Difference Distribution Table (DDT) and DC represents differential Cryptanalysis [9].

Complete design criteria of the S-box which we have used in designing of the PICO cipher is given below.
1. For any nonzero input difference $\Delta A \in F_2^4$ and output differences $\Delta B \in F_2^4$ respectively we have,

$$DC(\Delta A, \Delta B) = \# \{a \in F_2^4 | S(a) \oplus S(a \oplus \Delta A) = \Delta B \} \leq 4$$

2. For any nonzero input differences $\Delta A \in F_2^4$ and output differences $\Delta B \in F_2^4$ such that $\text{Hw}(\Delta A) = \text{Hw}(\Delta B) = 1$, we have,

$$\text{SetDC} = DC(\Delta A, \Delta B) = \#\{a \in F_2^4 | S(a) \oplus S(a \oplus \Delta A) = \Delta B \} = 0$$

Cardinality of SetDC can be given as $\text{CarDC}$, we have $\text{CarDC} = 2$.

This is the most important property in designing S-box. We have achieved Cardinality of 2 in both linear and differential table for the given S-box. This property indicates the strength and robustness of S-box.

3. For any nonzero input mask $A \in F_2^4$ and output mask such that $B \in F_2^4$ so we have $\text{LC}(A, B)$

$$\text{LC}(A, B) = \#\{a \in F_2^4 | A \cdot a = B \cdot S(a) \} - 8 \leq 4$$

4. For any nonzero input mask $A \in F_2^4$ and output mask such that $B \in F_2^4$, such that $\text{Hw}(A) = \text{Hw}(B) = 1$, we have

$$\text{SetLC} = \text{LC}(A, B) = \#\{x \in F_2^4 | A \cdot x = B \cdot S(x) \} - 8 \neq 0$$

Cardinality of SetLC can be given as $\text{CarLC}$, we have $\text{CarLC} = 2$.

5. Bijective i.e. $S(a) \neq S(b)$ for all values of $a \neq b$.

6. No static point i.e. $S(a) = a$ for all values of $a \in F_2^4$.

Strength of the S-box depends on cardinality. For PICO cipher, S-box has $\text{CarDC} = 2$ and $\text{CarLC} = 2$.

In the case of PRESENT cipher, S-box has $\text{CarDC} = 0$ and $\text{CarLC} = 8$ [8], while in case of RECTANGLE cipher, S-box has $\text{CarDC} = 2$ and $\text{CarLC} = 2$ [10]. This shows that PRESENT has a weak S-box, while RECTANGLE S-box shows good strength. As our designed S-box shows the good cardinality values, it shows that our S-box is good in design as compare to the PRESENT S-box.

B. PICO cipher S-Box Selection

Selection of S-box in PICO cipher is driven by two definitions

1) Permutation-then-XOR equivalence [10] [11]

If there exist a 4 X 4 permutation matrices $m_0$, $m_1$ and constant $A$, $B$ which belongs to $F_2^4$ for two S-boxes $S'(x)$ and $S(m_0(x) + A)$, such that $S'(x) = m_1(S(m_0(x) + A)) + B$, then the equivalence is called PE equivalence.

When S-box satisfies the criteria 1 to 5 from section A then its PE equivalent S-box also satisfies criteria 1 to 5.


If there exist a bijective linear mapping $A$, $B$ and constant $a$, $b$ belongs to $F_2^4$ for two S-boxes such that $S'(x) = B(S(A(x) + a)) + b$, then the equivalence called affine equivalence.

When S-boxes satisfies criteria 1, 3 and 5 from section A then the affine equivalent S-box also satisfies criteria 1, 3 and 5.

These two definitions are considered while designing the S-box for the PICO cipher.

C. Linear cryptanalysis

In cryptanalysis technique ‘Linear Cryptanalysis’ [8] is the most significant attack which is applicable on the symmetric-key block ciphers. This attack is also referred as the known plaintext attack. High probability occurrences of linear expression containing plaintext bits, cipher text bits and subkey bits are used for mounting the linear attack on cipher. Linear attack is mounted by having the knowledge about a subset of plaintext and its corresponding ciphertext. Attacker will find the correlation between them. S-box is examined by forming Linear Approximation Table (LAT) as mentioned in Table 3. Bias ($\varepsilon$) can be given as $|P_l - 1/2|$ where $P_l$ represents the linear probability. Maximum bias value for PICO cipher is $2^{-2}$. Matsui’s Piling-up lemma [8] is used to calculate the probability bias for ‘n’ rounds. The best way to resist against linear attack is mentioned below,

1] Optimizing the bias in LAT. For ideal S-box bias, values should be $1/8$ which is practically not possible to achieve.

2] Increase the number of active S-boxes in cipher structure.

Lemma 1: Matsui’s Pilling up Lemma [8]

For ‘n’ independent random binary variables $X_1$, $X_2$, … $X_n$, the equation is,

$$\varepsilon = 2^{-n-1} \prod_{i=1}^{n} |e_i|$$

Where $\varepsilon$ represents the total bias of $X_1 \oplus \ldots \oplus X_n = 0$ and $n$ represents number of active S-box.
Table 3 represents the minimum number of active S-boxes from linear trails.

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<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>A</td>
<td>0</td>
<td>0</td>
<td>-4</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>B</td>
<td>0</td>
<td>0</td>
<td>2</td>
<td>-2</td>
<td>-4</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>C</td>
<td>0</td>
<td>0</td>
<td>4</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>D</td>
<td>0</td>
<td>-4</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>E</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>F</td>
<td>0</td>
<td>-4</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
</tr>
</tbody>
</table>

Table 4 represents the minimum number of active S-boxes from linear trails.

<table>
<thead>
<tr>
<th>#Round</th>
<th>1</th>
<th>2</th>
<th>3</th>
<th>4</th>
<th>5</th>
<th>6</th>
</tr>
</thead>
<tbody>
<tr>
<td># Min. active S-boxes</td>
<td>1</td>
<td>2</td>
<td>4</td>
<td>6</td>
<td>8</td>
<td>11</td>
</tr>
</tbody>
</table>

Theorem 1:

For 24 rounds of PICO it has total 44 active S-boxes and total bias for 24 rounds is $2^{-45}$.

Proof:

For 6 rounds PICO has minimum 11 active S-boxes. Maximum bias for the PICO cipher S-box is $2^{-2}$ by using Matsui’s Piling up Lemma for 6 rounds of PICO cipher the total Bias can be given as

$$2^{10} \times (2^{-2})^{11} = 2^{-12}$$

For 24 rounds the total bias ($\epsilon$) can be given as

$$\epsilon = 2^{3} \times (2^{-12})^{4} = 2^{-45}$$

By calculating required number of known plaintext / ciphertext, we can compute complexity of linear attack and can be given as

$$N_L = 1/\epsilon^2 = 1/2^{-45} = 2^{40}$$

Available limit is $2^{64}$ and required no. of known plaintext is $2^{90}$, hence complete rounds of PICO cipher shows good resistance against a linear attack.

Table 5 shows the linear trails for PICO cipher.

<table>
<thead>
<tr>
<th>#Round</th>
<th>Input to S-box</th>
<th>Output from S-box</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>0001 0000 0000 0000</td>
<td>0000 0000 0000 0000</td>
</tr>
<tr>
<td>2</td>
<td>0000 0000 0000 0000</td>
<td>0000 0000 0000 0000</td>
</tr>
<tr>
<td>3</td>
<td>0000 0000 0000 0000</td>
<td>0000 0000 0000 0000</td>
</tr>
<tr>
<td>4</td>
<td>0000 0000 0000 0000</td>
<td>0000 0000 0000 0000</td>
</tr>
<tr>
<td>5</td>
<td>0000 0000 0000 0000</td>
<td>0000 0000 0000 0000</td>
</tr>
<tr>
<td>6</td>
<td>0000 0000 0000 0000</td>
<td>0000 0000 0000 0000</td>
</tr>
</tbody>
</table>

D. Differential Cryptanalysis

In cryptanalysis technique “Differential Cryptanalysis” [9] [12] is one of the most significant attack applicable to symmetric key block cipher. Differential Cryptanalysis firstly applied on DES by Biham and Shamir in 1990. Pair of high probability input and output occurrences are used to mount this attack. Substitution layer is a nonlinear layer in our design, which is examined by forming difference distribution table (DDT) as mentioned in Table 6.

Differential trails are formed by considering high probability input and output difference for each round, S-box that has non-zero input difference or non-zero output difference is referred as an active S-box.

$$P_D$$ represents differential probability, $$P_D$$ value for PICO S-box is $4/16 = 1/4 = 2^{-2}$. 
following two approaches,

1. Minimize the differential probability \( (P_d) \). For ideal S-box \( P_d = 1/16 \).

2. Build a cipher design such that it maximizes the minimum number of active S-boxes.

Table 7 represents the differential trails for PICO cipher. Nonzero input difference to S-box or non-zero output difference from S-box is referred as an active S-box.

There are 12 active S-boxes for 6 rounds of PICO cipher so for 24 rounds there are 48 active S-boxes. Differential probability for complete cipher is given as \( (2^{-96}) \).

Total number of chosen plaintext required to mount this attack can be given as,

\[
N_d = C/P_d
\]

Where \( C = 1 \) and \( P_d = 2^{-96} \), so the required number of chosen plaintext / ciphertext are,

\[
N_d = 1/2^{-96} = 2^{96}.
\]

E. Zero-Correlation attack

Zero-correlation attack [13] [14] is the extension of Linear Cryptanalysis. The block ciphers should resist zero correlation attack. Zero-correlation Attack is based on linear approximations with a correlation value of zero. Zero-correlation attack is considered as a counter part of impossible differential cryptanalysis in domain of linear cryptanalysis. We have applied matrix method [13] to mount Zero-correlation attack which is explained below. Following three Lemmas are used to find contradictions.

**Lemma 2: XOR approximation**

Either the three linear selection patterns at an XOR \( \oplus \) are equal or the correlation over \( \oplus \) is exactly zero.

**Lemma 3: Branching approximation**

Either the three linear selection patterns at a branching point • sum up to 0 or the correlation over • is exactly zero.

**Lemma 4: Permutation approximation**

Over a permutation \( \varphi \), if the input and output selection patterns are neither both zero nor both nonzero, the correlation over \( \varphi \) is exactly zero.

1) The Matrix Method [13]

Impossible differential characteristic can be constructed by using Miss-in-the-middle approach. Contradiction in middle is formed by two differential paths with probability.

Matrix method is used to find linear approximation with correlation zero and mentioned below,
The linear masks applied to the words can be of the following five types:
1. Zero mask denoted by 0,
2. An arbitrary non-zero mask denoted by $\bar{0}$,
3. Non-zero mask with a fixed value $a$,
4. The exclusive-or of a fixed non-zero mask $a$ and an arbitrary non-zero mask, denoted by $\bar{a}$,
5. Any other mask is denoted by $*$.

The matrix shows that how a linear mask of each output word is affected by the linear mask of an input word. Table 9 and 10 illustrate Arithmetic rules for multiplication and addition.

2) Zero-Correlation for 4 rounds of PICO
For $(0a00000000000000) \rightarrow (0000000000b000000)$ has correlation exactly zero for which the values $a$ and $b$ are non-zero. Trails for zero correlation attack are shown in Table 11 and we found contradiction at round 2 for PICO cipher. Red bits in table 11 shows contradiction.

F. Biclique attack
Biclique attack [15] [16] is an extension of meet-in-the-middle attack.

We have applied biclique cryptanalysis technique on PICO-128. From this result we have compare the result of PICO with other standard cipher algorithms like PRESENT, PICCOLO and LED.

We have constructed a 4-dimensional biclique for round 29 ~ 32 of PICO-128. For these rounds the partial keys used is $(K^{29}, K^{30}, K^{31}, K^{32})$ which is described as follows,

$K^{29} = k_{52}, k_{51}, \ldots, k_0, k_{63} \ldots k_{33}$
$K^{30} = k_{45}, k_{44}, \ldots, k_0, k_{63} \ldots k_{46}$
$K^{31} = k_{38}, k_{37}, \ldots, k_0, k_{63} \ldots k_{39}$
$K^{32} = k_{31}, k_{30}, \ldots, k_0, k_{63} \ldots k_{32}$
From above equation we found that by varying following sub keys \((k_{30}, k_{14}, k_{62}, k_{46})\) and \((k_{15}, k_{33}, k_{44}, k_{27})\) gives bicliques for the attack on the full PICO-128.

To construct the \(\Delta_i\)-differential we consider sub keys \((k_{30}, k_{14}, k_{62}, k_{46})\) and for the \(\nabla_j\)-differential, we consider sub keys \((k_{15}, k_{33}, k_{44}, k_{27})\). Let \(f\) be a sub-cipher from round 29 to round 32. Since the \(\Delta_i\)-differential affects the 48 bits of the ciphertext as illustrated from Fig. 5, as a result, the data complexity does not exceed \(2^{48}\). Red lines from the round in Fig 5 shows the data complexity.

\begin{equation}
C_{\text{total}} = 2^{k-2d} (C_{\text{biclique}} + C_{\text{precomp}} + C_{\text{recomp}} + C_{\text{falsepos}}).
\end{equation}

\begin{equation}
C_{\text{total}} = 2^{127.717}.
\end{equation}

G. Algebraic attack

Attacker applies algebraic attack [17] more usually on stream cipher because it is more successful on it rather than applying on block cipher. The \(4 \times 4\) bit S-box is be described...
by minimum 21 equations in 8 input or output variables. \( x = a \times 21 \) quadratic equation in \( y = a \times 8 \) variables are used to examine complete cipher. Where ‘a’ represents number of S-boxes used in encryption algorithm and key scheduling algorithm. In our cipher design for single round of encryption there are total 16 S-boxes that are used and for 128-bit key scheduling there are 48 S-boxes used. For 32 rounds of cipher, there are a total of \( 32 \times 16 = 512 \) S-boxes in the encryption system.

The number of quadratic equations that can be formed given as,

\[
a = (512) \times 21 = 10752
\]

and the number of variables can be given as

\[
b = (512) \times 8 = 4096.
\]

The 10752 number of quadratic equations in 4096 variables provide good resistance against the algebraic attack.

### H. Key schedule attacks

There is no specific instructions are provided to design key scheduling algorithm. So large number of multiple key scheduling algorithm can be formed and vide variety of key related attack can be mounted. Related key attack [18] and slide attack [19] are the two important attack mounted on key scheduling to check whether key scheduling is vulnerable or not. Related key attack is also known as chosen key attack and is successfully applied on reduced round AES-256 [20].

In this paper, we aimed at providing efficient and compact key scheduling which resist against the possibility of related key attack.

Recently, NSA has designed the lightweight ciphers named SIMON and SPECK which is optimized for hardware and software implementations [4]. There is no kind of key related attack found on SPECK key scheduling. In this paper, our key scheduling design is motivated from SPECK key scheduling.

### I. Key Collision Attack

This attack depends on the key length regardless of the key scheduling algorithm. Key collision attack [21] forms message with complexity \( 2^{k/2} \), where \( k \) denotes the length of key size. In our cipher design \( k = 128 \), so the complexity of created message is given as \( 2^{128/2} = 2^{64}. \)

### J. Avalanche Effect [22]

When a single bit change in the input changes the output significantly, this results in an avalanche effect. For example by flipping a single bit in the input or in a key could change the half of the bits in cipher text. Cipher with good avalanche effect has higher probability to resist all possible types of attacks.

In case of robust design of block ciphers, drastic change in the cipher text is visible when a small change in the key or the plaintext takes place. The poor randomization occurs when a block cipher does not show the avalanche effect to a significant degree.

In our design we have applied a single bit change in input plaintext / Key bits and observed the output. We found that in the case of the PICO cipher that any single bit change in key results in changes in more than half of the bits of cipher text. Table 12 summarizes the Avalanche Effect.

### IV. SECURITY COMPARISON WITH STANDARD ALGORITHM

In this section we have compared the security analysis of PICO with the other standard algorithms. The comparison is represented in Table 13 and 14. Table 13 compares the linear complexity and differential complexity by considering the number of active S-boxes for particular rounds.

### Table 12

<table>
<thead>
<tr>
<th>Plaintext</th>
<th>Key</th>
<th>Ciphertext</th>
<th>No. of bits Change</th>
</tr>
</thead>
<tbody>
<tr>
<td>0000 0000 0000 0000</td>
<td>0000 0000 0000 0000</td>
<td>fda7e7de58c913f4</td>
<td>--</td>
</tr>
<tr>
<td>0800 0000 0000 0000</td>
<td>0000 0000 0000</td>
<td>72f4081fae46ef5d</td>
<td>40</td>
</tr>
<tr>
<td>0400 0000 0000 0000</td>
<td>0000 0000 0000</td>
<td>f95da221c75ceb7</td>
<td>36</td>
</tr>
</tbody>
</table>

Table 12 summarizes the Avalanche Effect for PICO-128.

### Table 13

<table>
<thead>
<tr>
<th>Cipher Name</th>
<th>PICO</th>
<th>PRESENT</th>
<th>L-Block</th>
<th>FEW</th>
<th>PICCOLO</th>
</tr>
</thead>
<tbody>
<tr>
<td>#Rounds</td>
<td>24</td>
<td>25</td>
<td>15</td>
<td>27</td>
<td>30</td>
</tr>
<tr>
<td># Active S-box From Linear Trails</td>
<td>45</td>
<td>50</td>
<td>32</td>
<td>45</td>
<td>30</td>
</tr>
<tr>
<td># Active S-box From Differential Trails</td>
<td>48</td>
<td>50</td>
<td>32</td>
<td>45</td>
<td>30</td>
</tr>
<tr>
<td># Known Plaintext</td>
<td>2^{91}</td>
<td>2^{102}</td>
<td>2^{96}</td>
<td>2^{91}</td>
<td>2^{120}</td>
</tr>
<tr>
<td># Chosen Plaintext</td>
<td>2^{96}</td>
<td>2^{100}</td>
<td>2^{94}</td>
<td>2^{90}</td>
<td>2^{120}</td>
</tr>
<tr>
<td>Reference</td>
<td>This Paper</td>
<td>[1]</td>
<td>[26]</td>
<td>[27]</td>
<td>[3]</td>
</tr>
</tbody>
</table>

Table 14 compares the data complexity and computational complexity of PICO with the other ciphers.
### TABLE 14

<table>
<thead>
<tr>
<th>Cipher Name</th>
<th>Rounds</th>
<th>Data Complexity</th>
<th>Computational Complexity</th>
<th>Reference</th>
</tr>
</thead>
<tbody>
<tr>
<td>PICO-128</td>
<td>Full(32)</td>
<td>$2^4$</td>
<td>$2^{127.17}$</td>
<td>This Paper</td>
</tr>
<tr>
<td>PRESENT T-80</td>
<td>Full(31)</td>
<td>$2^3$</td>
<td>$2^{79.54}$</td>
<td>[16]</td>
</tr>
<tr>
<td>PRESENT T-128</td>
<td>Full(31)</td>
<td>$2^3$</td>
<td>$2^{127.42}$</td>
<td>[16]</td>
</tr>
<tr>
<td>PICO-80</td>
<td>Full(25)</td>
<td>$2^4$</td>
<td>$2^{70.13}$</td>
<td>[16]</td>
</tr>
<tr>
<td>PICO-128</td>
<td>Full(31)</td>
<td>$2^4$</td>
<td>$2^{127.35}$</td>
<td>[16]</td>
</tr>
<tr>
<td>LED-64</td>
<td>Full(48)</td>
<td>$2^4$</td>
<td>$2^{63.58}$</td>
<td>[16]</td>
</tr>
<tr>
<td>LED-80</td>
<td>Full(48)</td>
<td>$2^4$</td>
<td>$2^{79.37}$</td>
<td>[16]</td>
</tr>
<tr>
<td>LED-96</td>
<td>Full(48)</td>
<td>$2^4$</td>
<td>$2^{95.37}$</td>
<td>[16]</td>
</tr>
<tr>
<td>LED-128</td>
<td>Full(48)</td>
<td>$2^4$</td>
<td>$2^{127.37}$</td>
<td>[15]</td>
</tr>
</tbody>
</table>

Table 15 compares the S-box design consideration comparison with the lightweight ciphers. PICO S-box have $\text{CAR}_{\text{DC}} = 2$ and $\text{CAR}_{\text{LC}} = 2$ which illustrate that PICO cipher S-box is robust in design and provides good security than other lightweight ciphers.

### TABLE 15

<table>
<thead>
<tr>
<th>Cipher Name</th>
<th>Max. Val. in DDT</th>
<th>Max. Val. in LAT</th>
<th>$\text{CAR}_{\text{DC}}$</th>
<th>$\text{CAR}_{\text{LC}}$</th>
</tr>
</thead>
<tbody>
<tr>
<td>PICO</td>
<td>4</td>
<td>4</td>
<td>2</td>
<td>2</td>
</tr>
<tr>
<td>PRESENT</td>
<td>4</td>
<td>4</td>
<td>0</td>
<td>8</td>
</tr>
<tr>
<td>RECTANGLE</td>
<td>4</td>
<td>4</td>
<td>2</td>
<td>2</td>
</tr>
<tr>
<td>TWINE</td>
<td>4</td>
<td>4</td>
<td>5</td>
<td>7</td>
</tr>
</tbody>
</table>

V. HARDWARE AND SOFTWARE PERFORMANCE OF PICO CIPHER

Design of a PICO cipher provides optimum performance in software platform. We have considered the 32 bit ARM 7 LPC2129 [23] processor for analyzing software performance of PICO cipher.

Footprint area i.e. GEs are computed with standard cell library based on UMCL 180 0.18µ logic process (UMCL18G212T3) [24]. Memory size required for PICO cipher on 32 bit processor is 2504 bytes as Flash memory and 1256 as RAM memory. All other ciphers are written in embedded C and implemented on a 32 bit processor for comparison with the PICO cipher. Fig. 8 represents the memory comparison of the existing lightweight ciphers with PICO cipher. PICO needs less memory as compared to the other S-P network light weight ciphers.

Gate equivalent (GEs) is computed with UMCL180 standard cell library represented in Table 16.

GEs calculation for PICO cipher is represented in Table 17. GEs are counted as per the encryption algorithm mentioned in in Section I. For 128 bit key scheduling complete cipher need 1877 GEs.

### TABLE 16

<table>
<thead>
<tr>
<th>Standard Cell</th>
<th>Process</th>
<th>GE</th>
</tr>
</thead>
<tbody>
<tr>
<td>NOT</td>
<td>0.18µm</td>
<td>0.67</td>
</tr>
<tr>
<td>AND</td>
<td>0.18µm</td>
<td>1.33</td>
</tr>
<tr>
<td>XOR</td>
<td>0.18 µm</td>
<td>2.67</td>
</tr>
<tr>
<td>D F.F.</td>
<td>0.18 µm</td>
<td>6</td>
</tr>
</tbody>
</table>

### TABLE 17

<table>
<thead>
<tr>
<th>DATA Layer</th>
<th>GEs</th>
<th>%</th>
<th>KEY Layer</th>
<th>GEs</th>
<th>%</th>
</tr>
</thead>
<tbody>
<tr>
<td>D Reg.</td>
<td>384</td>
<td>20.4</td>
<td>K Reg.</td>
<td>768</td>
<td>40.89</td>
</tr>
<tr>
<td>S-Box</td>
<td>384</td>
<td>20.4</td>
<td>Shift Operator</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>P-Layer</td>
<td>0</td>
<td>0</td>
<td>S-box</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>XOR</td>
<td>170.8</td>
<td>9.09</td>
<td>XOR</td>
<td>170.88</td>
<td>9.09</td>
</tr>
<tr>
<td>Total</td>
<td>938.8</td>
<td>43.8</td>
<td>Total</td>
<td>938.88</td>
<td>56.11</td>
</tr>
</tbody>
</table>

Total number of gates required for 128-bit key = 1877.76

=1878
Fig. 10. Shows the GEs [23] comparison of other existing ciphers with PICO cipher. PICO results in consuming less GEs as compared to all the other existing lightweight ciphers.

We have calculated the power consumption by using X-power analyzer tool available in ISE design suit 14.2. Power is calculated with 10MHz frequency and on VIRTEX VI family. Table 18 represents dynamic power consumption of standard ciphers; PICO consumes 28mw power which is lesser than other lightweight ciphers.

Table 18

<table>
<thead>
<tr>
<th>Power Consumption of Lightweight Cipher</th>
<th>Standard Cell</th>
<th>Dynamic Power Consumption in mW</th>
</tr>
</thead>
<tbody>
<tr>
<td>PICO</td>
<td>28</td>
<td></td>
</tr>
<tr>
<td>PRESENT</td>
<td>31</td>
<td></td>
</tr>
<tr>
<td>LED</td>
<td>61</td>
<td></td>
</tr>
</tbody>
</table>

Table 19 shows the comparison of lightweight ciphers with PICO based on parameters like execution time, throughput and number of cycles required to convert plain text to cipher text. Throughput is computed on software platform at 12MHz.

Table 19

<table>
<thead>
<tr>
<th>Cipher</th>
<th>Block Size</th>
<th>Key Size</th>
<th>Execution Time (In uSec)</th>
<th>Throughput (In Kbps)</th>
<th>No. of Cycles</th>
</tr>
</thead>
<tbody>
<tr>
<td>SP NETWORK</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>ZORRO</td>
<td>128</td>
<td>128</td>
<td>913.21</td>
<td>140</td>
<td>10958.52</td>
</tr>
<tr>
<td>KLEIN</td>
<td>64</td>
<td>96</td>
<td>887.51</td>
<td>72</td>
<td>10650.12</td>
</tr>
<tr>
<td>PICO</td>
<td>64</td>
<td>128</td>
<td>4134.23</td>
<td>15.48</td>
<td>49610.76</td>
</tr>
<tr>
<td>PRESENT</td>
<td>64</td>
<td>128</td>
<td>2648.65</td>
<td>24.16</td>
<td>31783.8</td>
</tr>
</tbody>
</table>

| FEISTEL STRUCTURE |            |          |                          |                      |               |
| SPECK           | 64         | 128      | 49.02                    | 1305                 | 588.24        |
| SIMON           | 64         | 128      | 105.67                   | 605                  | 1288.04       |
| PICCOLO         | 64         | 128      | 227.68                   | 281                  | 2732.16       |
| CLEFIA          | 128        | 128      | 1048.01                  | 122                  | 12576.12      |
| TWINE           | 64         | 128      | 592.87                   | 108                  | 7114.44       |

VI. CONCLUSION

In this paper we have presented a PICO, an ultra-lightweight and low power cipher. PICO has a compact design which results in lesser foot print area and lower power consumption. PICO performs efficiently both on the hardware and the software platform. PICO achieves a great speed while encrypting the text, as it is based on S-P network as compared to the Feistel based ciphers. We have shown the resistance of PICO cipher against all types of possible attacks. During the cipher design we have done extensive computer based searches for good S-boxes, minimum number of active S-boxes and calculation of hamming weight for specific entries in the LAT and in the DDT. PICO cipher has a very strong S-box and a robust permutation layer which prevents the cipher design from undergoing the clustering of linear and differential trails. In designing PICO, we have achieved a very small gate count so that it can be implemented for security in any small scale embedded system. Through PICO cipher, we have tried to design the smallest block cipher of S-P network type and it is definitely better than any other lightweight S-P network cipher designed so far. PICO needs less GEs, less footprint area and less power consumption as compared to PRESENT cipher. Moreover PICO shows good resistance against attacks as compared to the PRESENT cipher. PICO has a very strong S-box and P-layer which makes the cipher design more robust as compared to PRESENT cipher. For applications like RFID tags, Wireless sensor nodes, where small GEs and power consumption play a crucial role, we believe PICO is the best suited design.

TEST VECTORSFOR 128 BIT KEY

| Plain Text: | 0000 0000 0000 0000 |
| Key:        | 0000 0000 0000 0000 0000 0000 0000 0000 |
| Cipher Text: | fda7e7de58c913f4 |
| Plain Text: | 0123 4567 89ab cdef |
| Key:        | 0000 0000 0000 0000 0000 0000 0000 0000 |
| Cipher Text: | 8ebcf66fd7289163 |

References


