# TABLE OF CONTENTS

| Acknowledgement                             | i-ii |
| List of Figures                             | iii  |
| List of Tables                              | iv-vi|
| List of Publications                        | v-vi |
| Abstract                                    | vii-ix|

## CHAPTER - 1. INTRODUCTION

1.1 Transceiver Systems                        | 1    |
1.2 RF Switches                               | 4    |
1.3 RF MOSFETs                                | 7    |
1.4 Issues of RF MOSFET Modeling              | 8    |
1.5 Organization of the Thesis                | 13   |

## CHAPTER - 2. DESIGN OF DOUBLE-POLE FOUR-THROW RF SWITCH

2.1 Introduction                              | 15   |
2.2 Design of CMOS Inverter for RF Switch     | 20   |
2.3 Design of DP4T RF Switch Based on Single-Gate MOSFET | 21   |
2.4 Operational Characteristics of DP4T CMOS Switch | 24   |
2.5 Topology for DP4T Switch                  | 26   |
2.6 Conclusions                               | 27   |

## CHAPTER - 3. ANALYSIS AND DESIGN OF DOUBLE-GATE MOSFET

3.1 Introduction                              | 28   |
3.2 Design of Double-Gate (DG) MOSFET         | 30   |
3.3 Effect of DG MOSFET on the Leakage Currents | 31   |
3.4 Performance Improvement of DG MOSFET Over SG MOSFET | 35   |
3.5 Resistive and Capacitive Model of DG MOSFET and SG MOSFET | 40   |
3.6 Characteristics of the DG MOSFET with Aspect Ratios | 45   |
3.7 Design of DG MOSFET with Several Gate-Fingers | 49   |
3.8 Computation of Flat-Band and Power Dissipation | 56   |
3.9 Conclusions                               | 59   |

## CHAPTER - 4. ANALYSIS AND DESIGN OF PROPOSED DP4T RF SWITCH BASED ON DG MOSFET

4.1 Introduction                              | 61   |
4.2 Characteristics of the Proposed DP4T DG RF CMOS Switch | 63   |
4.3 Effective ON-Resistance of DP4T DG RF CMOS Switch | 68   |
4.4 Attenuation of DP4T CMOS Switch 70
4.5 Off-Isolation 74
4.6 Resistive and Capacitive Model of DP4T DG RF CMOS Switch 75
4.7 Switching Speed of the Proposed DP4T DG RF CMOS Switch 76
4.8 S-Parameters of the DP4T DG RF CMOS Switch 77
4.9 Conclusions 80

CHAPTER - 5. ANALYSIS AND DESIGN OF CSDG MOSFET

5.1 Introduction 81
5.2 Design of CSDG RF MOSFET 84
5.3 Characteristics of CSDG MOSFET 89
5.4 Resistive and Capacitive model of the Proposed CSDG MOSFET 94
5.5 Advantages of the Proposed CSDG MOSFET Model 103
5.6 Conclusions 107

CHAPTER - 6. CONCLUSIONS AND FUTURE SCOPE

6.1 Conclusions 108
6.2 Future Scope 110

REFERENCES 112