List of Tables

1.1 List of some of the commodity multicore processors........................................... 10
3.1 Solo-run and concurrent-run performance of SPEC cpu2006 programs on Intel Xeon X5482 processor.......................................................... 34
3.2 Percentage degradation in performance of some of the SPEC cpu2006 programs during concurrent-run as compared to their solo-run on Intel Xeon X5482 processor.................................................. 36
3.3 Specifications of DELL Precision T7400 workstation used for generating data to build regression models................................................................. 45
3.4 Specifications of DELL Precision 390 workstation used for generating data to test regression models................................................................. 45
3.5 Sample of train-set data of program memory behavior from Intel Xeon X5482 processor................................................................. 51
3.6 Prediction accuracy metrics achieved in 10 times 10-fold cross validation for predicting solo-run last level cache stress on Intel Xeon X5482 processor................................................................. 54
3.7 Time taken for training the algorithms to build regression models for predicting solo-run last level cache stress......................................... 55
3.8 Last level cache related data of Intel Xeon X5482 and Intel Core2 6300 processors................................................................. 56
3.9 Prediction accuracy of trained models on test data from Intel Core2 6300 for predicting solo-run last level cache stress......................................... 57
4.1 Specifications of the experimental platform used for performance evaluation of intelligent scheduling done by meta-scheduler......................................... 67
4.2 Description of SPEC cpu2006 applications used in performance evaluation with meta-scheduler................................................................. 67
4.3 Sets of SPEC cpu2006 applications used for performance evaluation with meta-scheduler................................................................. 68
4.4 Characteristics of the workload sets used for performance evaluation with meta-scheduler................................................................. 68
4.5 $IPC$, $IPC_M$ and normalized speedup for various sets of workloads in 4-core experiments on Intel quad-core Xeon X5482 processor based platform. .................................................. 70

4.6 $IPC$, $IPC_M$ and normalized speedup for various sets of workloads in 8-core experiments on Intel quad-core Xeon X5482 processor based platform. .................................................. 71

5.1 Specifications of AMD quad-core Phenom 9650 processor based platform used for generating data to test regression models built for performance prediction. .................................................. 75

5.2 Specifications of AMD triple-core Phenom 8450 processor based platform used for generating data to test regression models built for performance prediction. .................................................. 75

5.3 Sample of train-set data for performance prediction collected from Intel Xeon X5482 processor. .................................................. 83

5.4 Prediction accuracy results from 10 times 10-fold cross validation for predicting concurrent-run performance on Intel Xeon X5482 processor. .................................................. 84

5.5 Time taken to build regression models using train-set having 3025 instances from Intel Xeon X5482 processor for predicting concurrent-run performance. .................................................. 84

5.6 Last level cache related data of Intel Xeon X5482, Intel Core2 6300, AMD Phenom 9650 and AMD Phenom 8450 processors used in performance prediction experiments. .................................................. 85

5.7 Prediction accuracy of the trained model on test data from Intel Core2 6300 processor for predicting concurrent-run performance. 86

5.8 Prediction accuracy of the trained model on test data from AMD Phenom 9650 processor for predicting concurrent-run performance. 86

5.9 Prediction accuracy of the trained model on test data from AMD Phenom 8450 processor for predicting concurrent-run performance. 86

5.10 Example solo-run performance data used by AKULA bootstrap module for multicore simulation. .................................................. 89

5.11 Example performance degradation matrix for concurrent-run, used by AKULA bootstrap module for multicore simulation. .................................................. 89