ACKNOWLEDGEMENT

The author expresses his gratitude to Dr. C. CHELLAMUTHU, Professor, School of Electrical and Electronics Engineering, Anna University for his invaluable guidance and constant encouragement in the conception, execution and completion of the research work.

The author expresses his thanks to Dr. M.A. PANNEER SELVAM, Dean of Electrical Engineering for providing the necessary facilities for the implementation of this work.

The author is deeply indebted to the authorities of Annamalai University, Annamalainagar for sponsoring him to undertake this research programme at Anna University, Chennai. The author takes this opportunity to express his sense of indebtedness also to Mr. R. VIJAYA RAJESWARAN, Mr. K.K. MATHEW, Mr. KUMAR BABU BATTU, Mr. R. MUTHUKUMARAN, Mr. R. THIRUNAVUKARASU, Mr. R.VENKATESH, Mr. BIPUL KARKI and Mr. V. Senthil Kumar for their help at various stages of this research work. The author is grateful to Mrs. G. UMA and Mr. S. MUTHUKUMAR, Lecturers, School of Electrical and Electronics Engineering, Anna University for their help at very crucial times.

The author also thanks the teaching and the non-teaching staff of School of Electrical and Electronics Engineering, Anna University for their co-operation in carrying out the research work.

The author finally acknowledges the support from his wife Mrs. NIRMALA and his son VIJAY GANESH who have sacrificed some of the best years of their life in hours of neglect so that he could successfully complete this work.

(S.P. NATARAJAN)