References


[44]. Karpovsky, M., Kulikowski, K.J., and Taubin, A., “Robust protection against fault-injection attacks on smart cards implementing the advanced encryption standard,”


[64]. Pawel Chodowiec, Kris Gaj, Peter Bellows and Brian Schott (2001) presented Experimental Testing of the Gigabit IPSec-Compliant Implementations of Rijndael and Triple DES Using SLAAC-1V FPGA Accelerator Board.


[74]. Samir ElAdib and Naoufal Raissouni (2012) presented AES Encryption Algorithm Hardware Implementation Architecture:


List of Publications


